

Summary

- **RoHS Compliant & Halogen Free**
- **Applications:** All high-density boards
- **Product Features:** Small surface mountable , Solid state , Faster time to trip than standard SMD devices, Lower resistance than standard SMD devices
- **Operation Current:** 0.05A~2.0A
- **Maximum Voltage:** 6V~ 60V_{DC}
- **Temperature Range :** -40°C to 85°C

Agency Recognition

UL : File No. E211981
 C-UL: File No. E211981
 TÜV: File No. R50090556

Electrical Characteristics (23°C)

Part Number	Hold	Trip	Rated	Max	Typical	Max Time to Trip		Resistance	
	Current	Current	Voltage	Current	Power	Current	Time	R _{MIN}	R _{1MAX}
	I _H , A	I _T , A	V _{MAX} , VDC	I _{MAX} , A	P _d , W	Amp	Sec	Ohms	Ohms
TSMD005-1210	0.05	0.15	60	100	0.60	0.25	3.00	3.600	50.000
TSMD005-1210-R	0.05	0.15	60	100	0.60	0.25	3.00	3.600	50.000
TSMD010-1210	0.10	0.25	60	100	0.60	0.50	1.50	1.600	15.000
TSMD010-1210-R	0.10	0.25	60	100	0.60	0.50	1.50	1.600	15.000
TSMD020-1210	0.20	0.40	30	100	0.60	8.00	0.02	0.800	5.000
TSMD020-1210-R	0.20	0.40	30	100	0.60	8.00	0.02	0.800	5.000
TSMD035-1210	0.35	0.70	16	100	0.60	8.00	0.20	0.320	1.300
TSMD035-1210-R	0.35	0.70	16	100	0.60	8.00	0.20	0.320	1.300
TSMD050-1210	0.50	1.00	16	100	0.60	8.00	0.10	0.250	0.900
TSMD050-1210-R	0.50	1.00	16	100	0.60	8.00	0.10	0.250	0.900
TSMD075-1210	0.75	1.50	8	100	0.60	8.00	0.10	0.130	0.400
TSMD075-1210-R	0.75	1.50	8	100	0.60	8.00	0.10	0.130	0.400
TSMD075-24-1210R	0.75	1.50	24	100	0.60	8.00	0.10	0.130	0.400
TSMD110-1210R	1.10	2.20	6	100	0.80	8.00	0.30	0.060	0.210
TSMD150-1210R	1.50	3.00	6	100	0.80	8.00	0.50	0.040	0.110
TSMD175-1210R	1.75	4.00	6	100	0.80	8.00	0.60	0.020	0.080
TSMD200-1210R	2.00	4.00	6	100	0.80	8.00	1.00	0.015	0.070

I_H=Hold current-maximum current at which the device will not trip at 23°C still air.

I_T=Trip current-minimum current at which the device will always trip at 23°C still air.

V_{MAX}=Maximum voltage device can withstand without damage at it rated current.(I_{MAX})

I_{MAX}= Maximum fault current device can withstand without damage at rated voltage (V_{MAX}).

P_d=Typical power dissipated-type amount of power dissipated by the device when in the tripped state in 23°C still air environment.

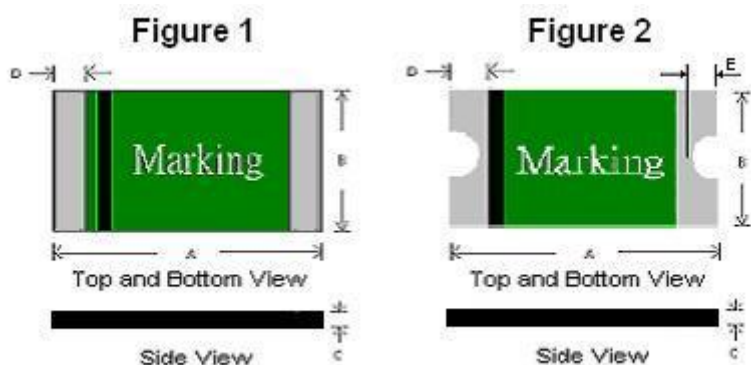
R_{MIN}=Minimum device resistance at 23°C prior to tripping.

R_{1MAX}=Maximum device resistance at 23°C measured 1 hour after tripping or reflow soldering of 260°C for 20 seconds.

Termination pad characteristics

Termination pad materials: Pure Tin

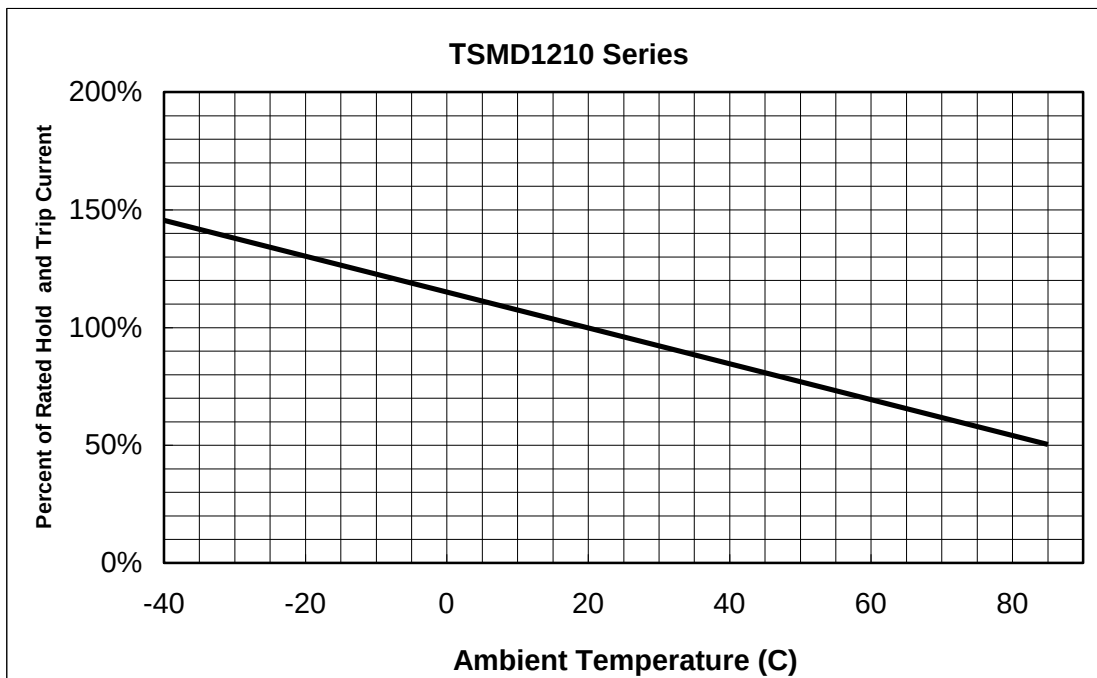
NOTE : Specification subject to change without notice.

TSMD Product Dimensions (Millimeters)


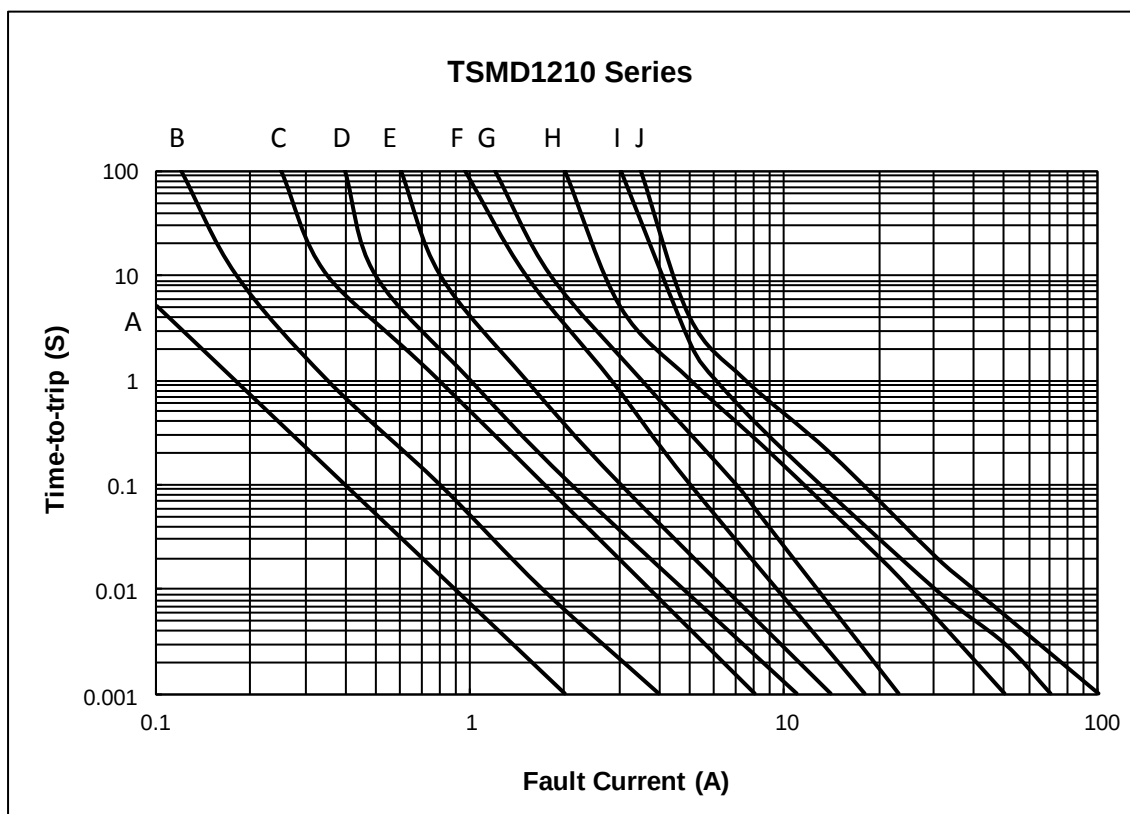
Part Number	Figure	A		B		C		D		E	
		Min	Max	Min	Max	Min	Max	Min	Max	Min	Max
TSMD005-1210	1	3.00	3.43	2.35	2.80	0.60	1.15	0.25	0.75	—	—
TSMD005-1210-R	2	3.00	3.43	2.35	2.80	0.60	1.15	0.25	0.75	0.10	0.45
TSMD010-1210	1	3.00	3.43	2.35	2.80	0.60	1.15	0.25	0.75	—	—
TSMD010-1210-R	2	3.00	3.43	2.35	2.80	0.60	1.15	0.25	0.75	0.10	0.45
TSMD020-1210	1	3.00	3.43	2.35	2.80	0.40	0.85	0.25	0.75	—	—
TSMD020-1210-R	2	3.00	3.43	2.35	2.80	0.40	0.85	0.25	0.75	0.10	0.45
TSMD035-1210	1	3.00	3.43	2.35	2.80	0.40	0.80	0.25	0.75	—	—
TSMD035-1210-R	2	3.00	3.43	2.35	2.80	0.40	0.80	0.25	0.75	0.10	0.45
TSMD050-1210	1	3.00	3.43	2.35	2.80	0.30	0.75	0.25	0.75	—	—
TSMD050-1210-R	2	3.00	3.43	2.35	2.80	0.30	0.75	0.25	0.75	0.10	0.45
TSMD075-1210	1	3.00	3.43	2.35	2.80	0.30	0.70	0.25	0.75	—	—
TSMD075-1210-R	2	3.00	3.43	2.35	2.80	0.30	0.70	0.25	0.75	0.10	0.45
TSMD075-24-1210R	2	3.00	3.43	2.35	2.80	0.90	1.30	0.25	0.75	0.10	0.45
TSMD110-1210R	2	3.00	3.43	2.35	2.80	0.60	1.00	0.25	0.75	0.10	0.45
TSMD150-1210R	2	3.00	3.43	2.35	2.80	0.50	0.90	0.25	0.75	0.10	0.45
TSMD175-1210R	2	3.00	3.43	2.35	2.80	0.80	1.40	0.25	0.75	0.10	0.45
TSMD200-1210R	2	3.00	3.43	2.35	2.80	0.80	1.40	0.25	0.75	0.10	0.45

NOTE : Specification subject to change without notice.

Thermal Derating Curve



A =TSMD005-1210 /-R
 B =TSMD010-1210 /-R
 C =TSMD020-1210 /-R
 D =TSMD035-1210 /-R
 E =TSMD050-1210 /-R
 F =TSMD075-1210 /-R
 TSMD075-24-1210R
 G =TSMD110-1210R
 H =TSMD150-1210R
 I =TSMD175-1210R
 J =TSMD200-1210R



NOTE : Specification subject to change without notice.

Material Specification

Terminal pad material: Pure Tin

Soldering characteristics: Meets EIA specification RS 186-9E, ANSI/J-std-002 Category 3

Part Numbering and Marking System

Part Numbering System

TSMD□□□-1210 R
 └──────────┘
 Current Rating

TSMD□□□-1210-R
 └──────────┘
 Current Rating

TSMD□□□-□□-1210 R
 └──────────┘ └──────────┘
 Current Rating Voltage Rating

Part Marking System

F75
 Example

F □ □
 └──────────┘
 Part Identification
 PPTC Fuse

F75
 Example

F □ □
 └──────────┘
 Part Identification
 PPTC Fuse

F75
 F
 Example

F □ □
 └──────────┘
 Part Identification
 PPTC Fuse

Warning: -Operation beyond the specified maximum ratings or improper use may result in damage and possible electrical arcing and/or flame.

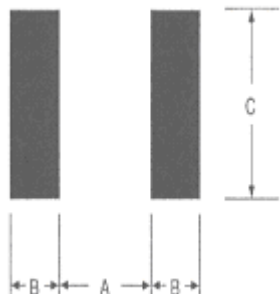


-PPTC device are intended for occasional overcurrent protection. Application for repeated overcurrent condition and/or prolonged trip are not anticipated.

-Avoid contact of PPTC device with chemical solvent. Prolonged contact will damage the device performance.

Pad Layouts 、 Solder Reflow and Rework Recommendations

The dimension in the table below provide the recommended pad layout for each FSMD1210 device



Pad dimensions (millimeters)

Device	A Nominal	B Nominal	C Nominal
All 1210 Series	2.00	1.00	2.80

Profile Feature	Pb-Free Assembly
Average Ramp-Up Rate (T_{smax} to T_p)	3 °C/second max.
Preheat :	
Temperature Min (T _{smin})	150 °C
Temperature Max (T _{smax})	200 °C
Time (t _{smin} to t _{smax})	60-180 seconds
Time maintained above:	
Temperature(T _L)	217 °C
Time (t _L)	60-150 seconds
Peak/Classification Temperature(T_p) :	260 °C
Time within 5°C of actual Peak :	
Temperature (t _p)	20-40 seconds
Ramp-Down Rate :	6 °C/second max.
Time 25 °C to Peak Temperature :	8 minutes max.

Note 1: All temperatures refer to of the package,
measured on the package body surface.

Solder reflow

※ Due to “Lead Free” nature, Temperature and Dwelling time for the soldering zone is higher than those for Regular. This may cause damage to other components.

1. Recommended max past thickness > 0.25mm.
2. Devices can be cleaned using standard methods and aqueous solvent.
3. Rework use standard industry practices.
4. Storage Environment : < 30°C / 60%RH

Caution:

1. If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.
2. Devices are not designed to be wave soldered to the bottom side of the board.

